

Department of Electronics and Communications Engineering

Academic Year: 2024-25

Value-Added Courses

S.No.	Name of the value added course	Year & Sem	Year of offering	Date (from-to)	No. of contact Hours	No. of students enrolled	Resource Person	In association with
1	Linux Administration	II B.Tech & I Sem	2024	27-08-2024 to 31-08-2024	40	173	Mr. S SAIKRISHNA TEJA, Team Lead, Edge Networks LAKSHMAN MALLIDI, Data Engineer, Vimeo Linux Experts	SWECHA, AP Team, Vijayawada
2	IoT application development using low cost SBCs as Edge and it's advantages	III B.Tech & I Sem	2024	07-10-2024 to 15-10-2024	48	164	Mr.M.Muralidhar, Senior consultant on AI/ML	Hitachi, USA
3	PCB Design and Fabrication	II B.Tech & II Sem	2025	10-02-2025 to 14-02-2025	35	174	Mr. R. Vikas Reddy and Team, Founder & CEO	Technotran, Nellore.

4	Hands on Projects on Raspberry Pi	III B.Tech & II Sem	2025	03-03-2025 to 07-03-2025	40	161	Mr.S.Rakesh Rajan, Embedded Developer	Takeoff Edu Group, Tirupati
5	Fundamentals of Analog VLSI & Hands on using LT Spice (IOC)	III B.Tech & II Sem	2025	17-03-2025 to 21-03-2025	18	160	Dr K Vasudeva Reddy, Principal Engineer	Global Foundries Bangalore

Faculty Development Program Organized

S.no	Name of the Program	Title of the Program	Date	No. Of Participants
1	FDP	VLSI design cycle :Physical design Engineering	19.12.2024 to 15.12.2024	28
2	FDP	Recent advancements and Challenges in applications of Artificial Intelligence in VLSI	15.06.2025 to 20.06.2025	40

Guest Lectures

S.No	Title	Date of Conduct	Resource Person
1.	Fundamentals analog VLSI	21/03/2025	Dr.K.Vasudeva Reddy
2.	Stochastic Process	16/10/2025	Dr.P.Giriprasad
3.	Lowpower RISC MSP430	17/10/2025	V.Praveen Kumar

Internships

S.NO	Company/Organization	Student Strength
1	CodTech IT Solutions	75
2	Technotran Solutions	78
3	Slash Mark	10
4	Innovative	5
5	Ei System	2
6	HEXAIND Technoligies	1
7	Brain O Vision	1
	Total	172

Circuit Club

S.No	Date	Title of the Event	Year/Class	Venue	No of Participant
1	06/11/2024	Circuit Debugging Session	II ECE, EVT, ACT,IIIECE	Project Lab	18

Bridge Courses

S.No.	Academic year-Sem	Year-Branch	Subject Name	Staff
1	2024-25 (I Sem)	IV ECE	Basic Concepts of VLSI Design	Mr.K.V.Bhanu Prakash
2			Introduction to field Programmable Gate Array	Mrs.M.Divya Vani
3	2023-24 (II Sem)	III ECE	Fundamentals of Digital Signal Processing	Mrs.Y.Neeraja
4		II ECE	Introduction to Electromagnetic Theory & Transmission Lines	Dr.B.Malakonda Reddy
5		III ECE	Basic Concepts of VLSI Design	Mr.S.Brahmaiah